

Amendments to the Claims:

This listing of the claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

1. (Currently amended) An apparatus comprising:
a basic device including a docking interface; and
an accessory device, including a control processor and a power supply unit, that couples to the docking interface of the basic device;
wherein the power supply unit supplies electrical energy to the control processor in response to a control signal received from the basic device and maintains the electrical energy during fluctuations of the control signal;
wherein the control signal comprises a signal providing information indicative of whether or not an application involving use of the accessory device is currently running on the basic device.
2. (Original) An apparatus as claimed in claim 1, wherein the power supply unit maintains the electrical energy supplied to the control processor in response to a further control signal received from the control processor.
3. (Original) An apparatus as claimed in claim 2, wherein the power supply unit includes a power management circuit that receives the control signal from the basic device and the further control signal from the control processor, and a power supply that supplies the electrical energy to the control processor.
4. (Original) An apparatus as claimed in claim 3, wherein the power management circuit includes a first switching element that is responsive to the control signal and the further control signal to generate a power activation signal, and a second switching element that is responsive to the power activation signal.

5. (Original) An apparatus as claimed in claim 4, wherein the accessory device further includes at least one battery, and the second switching element couples the battery to the power supply in response to the power activation signal.

6. (Original) An apparatus as claimed in claim 4, wherein the first switching element comprises a bipolar transistor and the second switching element comprises a field effect transistor.

7. (Original) An apparatus as claimed in claim 3, wherein the power supply comprises a switched mode power supply.

8. (Original) An apparatus as claimed in claim 1, wherein the basic device comprises a personal digital assistant device.

9. (Original) An apparatus as claimed in claim 8, wherein the accessory device comprises a digital camera.

10. (Original) An apparatus as claimed in claim 9, wherein the control processor controls the operation of the digital camera to capture image data in response to a control signal received from the basic device.

11. (Currently amended) An apparatus as claimed in claim 9, wherein the personal digital ~~assistant~~ assistant includes a display device, and wherein image data captured by the digital camera is displayed on the display device of the personal digital assistant.

12. (Currently amended) An apparatus as claimed in claim 1, wherein the power supply unit includes a power management circuit comprising a capacitor and resistor network that receives the control signal from the basic device, the capacitor and resistor network comprising at least one capacitor and at least one resistor, and a power supply coupled to the power management circuit, and wherein said capacitor and

resistor network maintains ~~a latch~~ an input of the power supply ~~to~~ at a logic level required to maintain the electrical energy when the control signal fluctuates.

13. (Currently amended) A digital camera accessory device comprising:
a lens system;
a docking interface;
image processing circuitry that captures image data;
a control processor that controls the operation of the image processing circuitry to perform an image capture operation; and
a power supply unit that supplies electrical energy to the image processing circuitry and the control processor;
wherein the power supply unit supplies electrical energy to the control processor in response to a control signal received from the docking interface and maintains the electrical energy during fluctuations of the control signal; and
wherein the control signal must have one or more predetermined signal characteristics in order for the digital camera accessory device to transition from a powered-off state to a powered-on state when coupled via the docking interface to a basic device.

14. (Original) A digital camera as claimed in claim 13, wherein the power supply unit maintains the electrical energy supplied to the control processor in response to a further control signal received from the control processor.

15. (Original) A digital camera as claimed in claim 14, wherein the power supply unit includes a power management circuit that receives the control signal and the further control signal and a power supply that supplies the electrical energy to the control processor.

16. (Original) A digital camera as claimed in claim 15, wherein the power management circuit includes a first switching element that is responsive to at least one of the control signal and the further control signal to generate a power

activation signal, and a second switching element that is responsive to the power activation signal.

17. (Original) A digital camera as claimed in claim 16, wherein the digital camera further includes at least one battery, and the second switching element couples the battery to the power supply in response to the power activation signal.

18. (Original) A digital camera as claimed in claim 16, wherein the first switching element comprises a bipolar transistor and the second switching element comprises a field effect transistor.

19. (Original) A digital camera as claimed in claim 15, wherein the power supply comprises a switched mode power supply.

20. (Currently amended) An apparatus as claimed in claim 13, wherein the power supply unit includes a power management circuit comprising a capacitor and resistor network that receives the control signal from the ~~basic device~~ docking interface, the capacitor and resistor network comprising at least one capacitor and at least one resistor, and a power supply coupled to the power management circuit, and wherein said capacitor and resistor network maintains ~~latches~~ an input of the power supply ~~to~~ at a logic level required to maintain the electrical energy when the control signal fluctuates.

21. (Currently amended) A method of managing the power requirements of an accessory device coupled to a basic device comprising:

generating a first control signal with the basic device and supplying the first control signal to the accessory device;

activating a power supply unit ~~to~~ of the accessory device in response to the first control signal to supply electrical power from the power supply unit to a control processor of the accessory device;

generating a second control signal with the control processor of the accessory device and supplying the second control signal to the power supply unit; and latching operation of the power supply unit in response to the second control signal to maintain the supply of electrical power from the power supply unit to the control processor regardless of the state of the first control signal.

22. (Original) A method as claimed in claim 21, further comprising maintaining the latching of the operation of the power supply unit for a predetermined time period.

23. (Original) A method as claimed in claim 22, further comprising discontinuing the latching of the operation of the power supply unit after expiration of the predetermined time period in response to the state of the first control signal.

24. (Original) A method as claimed in claim 23, further comprising performing an accessory operation with the accessory device in response to an activity command signal and resetting the predetermined time period after completion of the accessory operation.